

Fig. 1 Main claim from PF 54.059

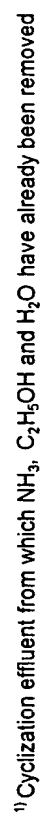


Fig. 2 Contin. performance of example 7, D2 schematic; simplified